



REVISION HISTORY				
REV	ECO NO	DESCRIPTION	DATE	APVD
n/a	----	DEV. PROTOTYPE	02/06/2015	XXX

— Patch PCB Layer 1
— Patch PCB Layer 2
— Patch PCB Layer 3

KYMETA CORP.

12277 134TH COURT NE SUITE 100 REDMOND, WA 98052
425-889-3700

Title: **10 x 5 Cell Matrix**

Sheet Name: **TI Dual Driver FET Switch and Cap**

SIZE: B

DWG Number: PCBNUMBER

REV: BREV

Date: 1/20/2016

Time: 3:56:29 PM

Sheet 1 of 1